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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Reithinger, et al.

Attorney Docket: 00P7685US

Filed:

June 16, 2000

Examiner:

Phat X. Cao

Serial No.:

09/596,129

Art Unit:

2814

For:

Multiple Chip Semiconductor Arrangement having Electrical Components in

Separating Regions

Mail Stop: RCE

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R.§ 1.97(c)(1). Applicant hereby states that each item of information contained in this statement was first cited in a communication from a foreign patent office in a counterpart application not more than three months prior to the filing of this statement. As a result, no fee is due at this time. A copy of the search report is provided for the Examiner's convenience.

Respectfully submitted,

January 19, 2004

Date

Yames C. Kesterson 'Attorney for Applicants

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PTO/SB/08A (05-03)

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Substitute for form 1449A/PTO				C mpl te if Known			
				Application Number	09/596,129		
	INFORMATION	DISC	LOSURE	Filing Date	June 16, 2000 Reithinger, et al.		
	STATEMENT BY	/ APF	PLICANT	First Named Inventor			
				Art Unit	2814		
	(use as many shee	ts as ne	ecessary)	Examiner Name	Phat X Cao		
Shee	t 1	of	1	Attorney Docket Number	00P7685US		

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	No.1	Number - Kind Code ^{2 (if known)}	MM-DD-YYYY	Applicant of Cited Document		
	1	US-6,055,655	04-25-2000	Momohara		
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		FOREIG	N PATENT DOCU	IMENTS		
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ₆
	2	JP-01-220843	09-04-1989	NEC Corp.	English Abstract only	
	3	JP-63-234553	09-29-1988	NEC Corp.	English Abstract only	1
	4	JP-04-171860	06-19-1992	Hitachi Ltd.	English Abstract only	1
	5	JP-11-163062	06-18-1999	Toshiba Corp.	English Abstract only	1
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OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite, No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²		

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 'Applicant's unique citation designation number (optional). 'See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 'Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 'For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 'Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 'Applicant is to place a check mark here if English language Translation is attached.

Translation is attached.

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